

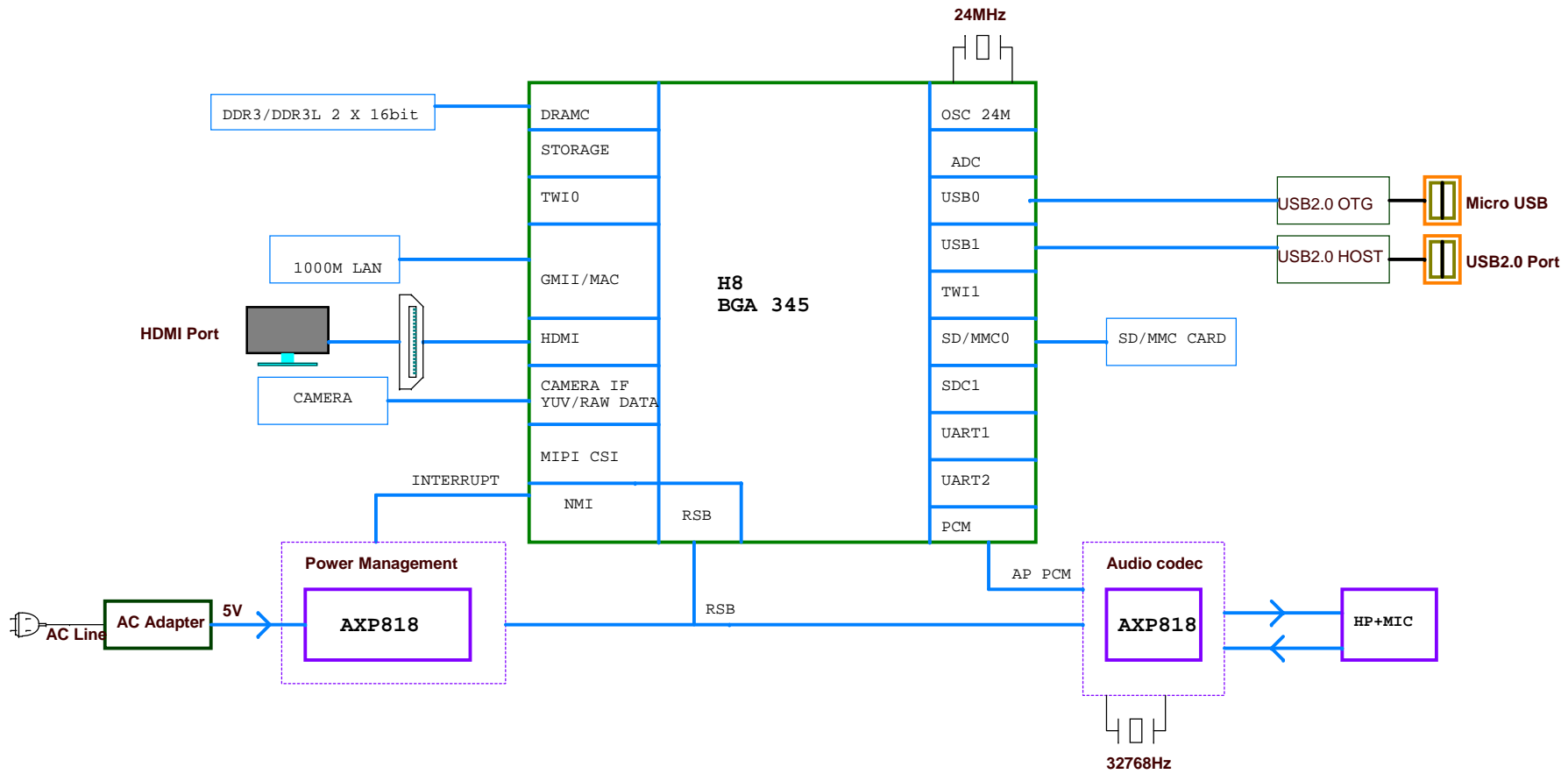
REVISION HISTORY

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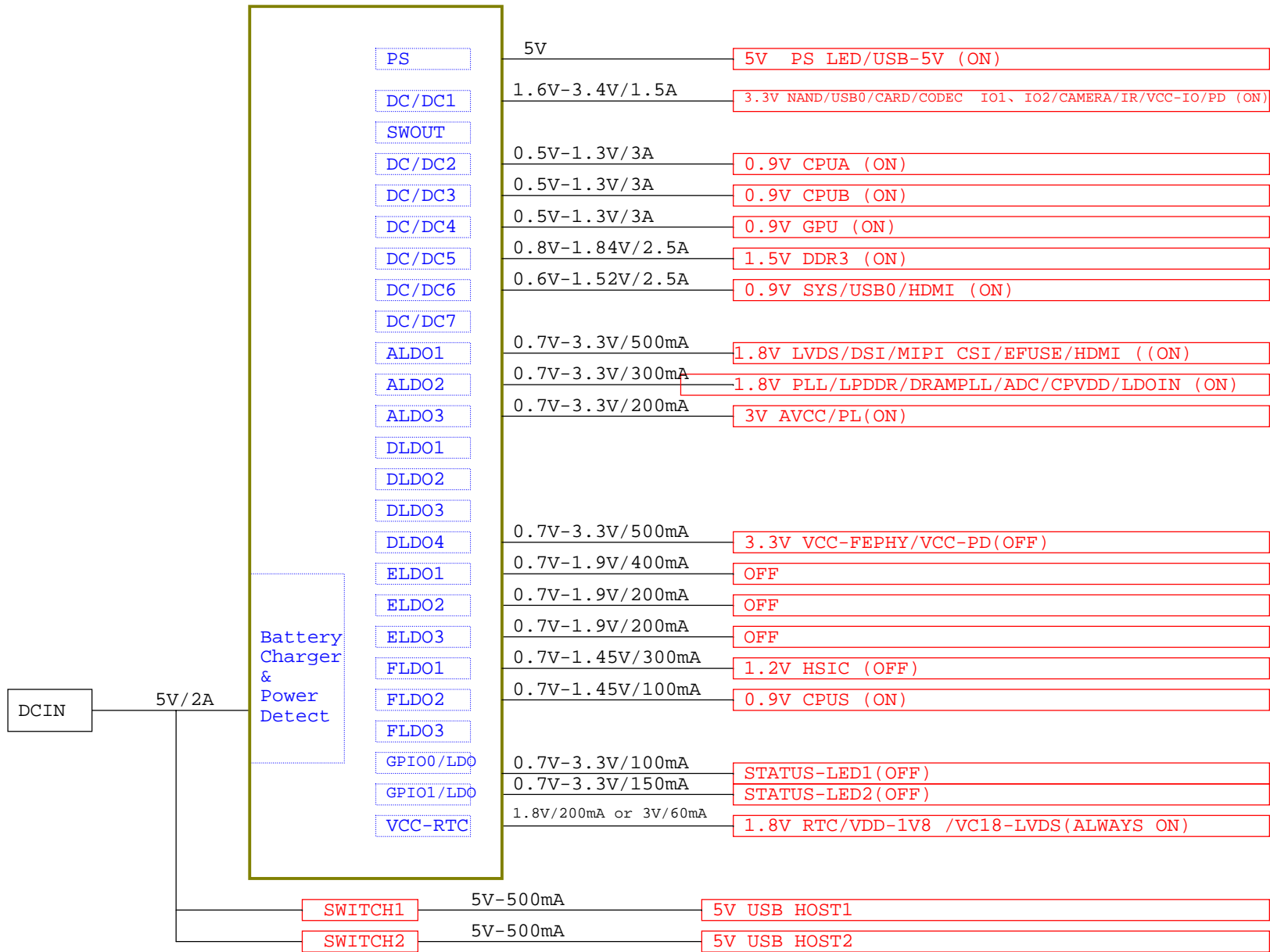
Revision	Description	Date	Drawn	Checked
Ver 01	Draft	2015.07.29		

BLOCK DIAGRAM



POWER TREE

AXP818



GPIO ASSIGNMENT

PIN	Define	Function
PB0	UART2_TX	GPIO1
PB1	UART2_RX	GPIO2
PB2	UART2_RTS	GPIO3
PB3	UART2_CTS	
PB4	I2S-LRCK	I2S
PB5	I2S-BCLK	
PB6	I2S-DOUT	
PB7	I2S-DIN	
PB8	I2S-MCLK	
PB9	UART0-TX	UART0
PB10	UART0-RX	DEBUG

PIN	Define	Function
PC0	NWE	NC
PC1	NALE	
PC2	NCLK	
PC3	NCE1	
PC4	NCE0	
PC5	NRE/CLK	
PC6	NRB0/CMD	
PC7	NRB1	
PC8	NDQ0/D0	
PC9	NDQ1/D1	
PC10	NDQ2/D2	
PC11	NDQ3/D3	
PC12	NDQ4/D4	
PC13	NDQ5/D5	
PC14	NDQ6/D6	
PC15	NDQ7/D7	
PC16	NDQS/RST	
PC17	NCE2	
PC18	NCE3	

PIN	Define	Function
PD2	GRXD3	GMII EMAC
PD3	GRXD2	
PD4	GRXD1	
PD5	GRXD0	
PD6	GRXCK	
PD7	ERXDV	
PD10	ERXERR	
PD11	GTXD3	
PD12	GTXD2	
PD13	GTXD1	
PD14	GTXD0	
PD15	ECRS	
PD18	ETXCK	
PD19	ETXEN	
PD20	ETXERR	
PD21	ECOL	
PD22	GMDC	
PD23	GMDIO	
PD24	TX-LED	GPIO
PD25	PWM3	
PD26	PWM4	
PD27	GPIO0	
PD28	PWM2	
PD29	RX-LED	GPIO

PIN	Define	Function
PE0	AP-CSI-PLK	CAMERA IF
PE1	CSIO-MCLK	
PE2	AP-CSI-HSYNC	
PE3	AP-CSI-VSYNC	
PE4	GM-RST1	
PE5	CSIO-PWR-EN	
PE6	AP-CSI-D2	
PE7	AP-CSI-D3	
PE8	AP-CSI-D4	
PE9	AP-CSI-D5	
PE10	AP-CSI-D6	
PE11	AP-CSI-D7	
PE12	AP-CSI-D8	
PE13	AP-CSI-D9	
PE14	CSI-SCK	
PE15	CSI-SDA	
PE16		
PE17		
PE18		
PE19		

PIN	Define	Function
PF0	SDC0-D1	SDIO0 TF CARD
PF1	SDC0-D0	
PF2	SDC0-CLK	
PF3	SDC0-CMD	
PF4	SDC0-D3	
PF5	SDC0-D2	
PF6	SDC0-DET	

PIN	Define	Function
PG0		GPIO
PG1		
PG2		
PG3		
PG4		SPI1
PG5		
PG6	SPI1_CS	
PG7	SPI1_CLK	
PG8	SPI1_MOSI	UART3
PG9	SPI1_MISO	
PG10	UART3-TX	
PG11	UART3-RX	
PG12		
PG13		

PIN	Define	Function
PH0	TWI0-SCK	CONN
PH1	TWI0-SDA	
PH2	TWI1-SCK	CONN
PH3	TWI1-SDA	
PH4	TWI2-SCK	HDMI
PH5	TWI2-SDA	
PH6	HSCL	
PH7	HSDA	
PH8	HCEC	
PH9		
PH10		
PH11		

PIN	Define	Function
PL0	PMU-SCK	PMU+
PL1	PMU-SDA	CODEC
PL2	S_UART_TX	CPU
PL3	S_UART_RX	DEBUG
PL4		
PL5	USB0-DRVVBUS	USB0_EN
PL6		
PL7		
PL8		
PL9	RECOVERY	KEY
PL10	PWM1	S_PWM
PL11	IRQ-AUDIO	CODEC
PL12	IR-RX	IR

AXP818

PIN	Define	Function
GPIO0	STATUS-LED1	LED
GPIO1	STATUS-LED2	

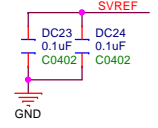
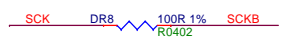
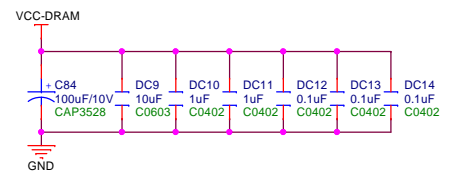
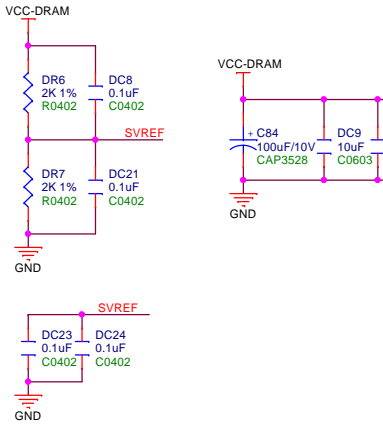
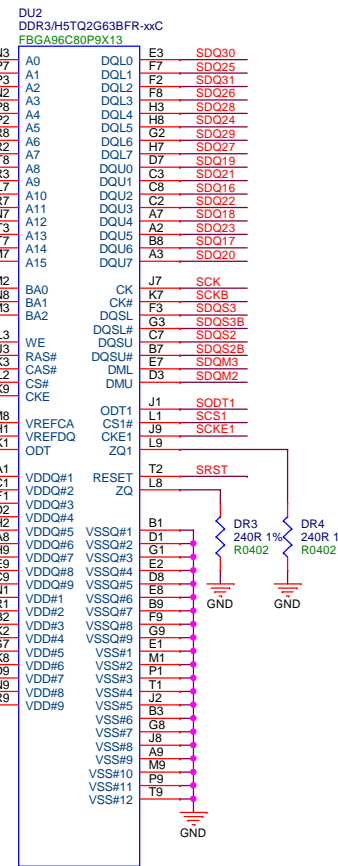
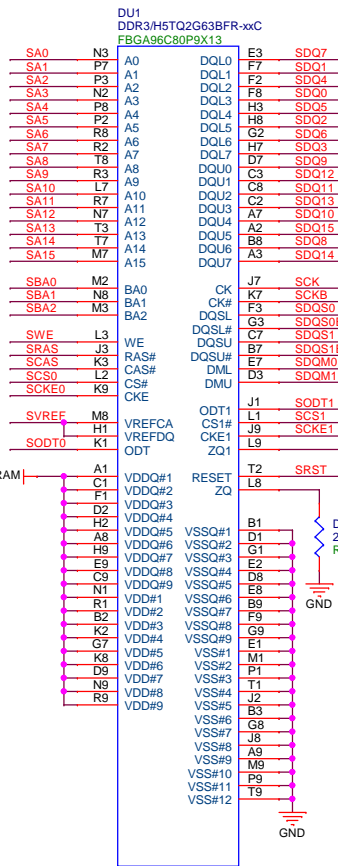
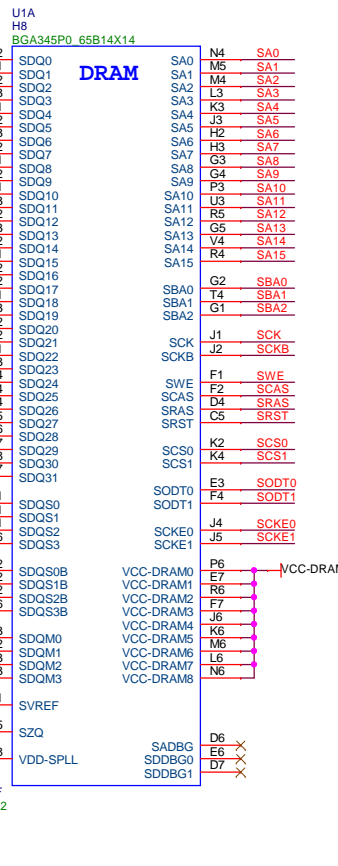
DDR3_16X2

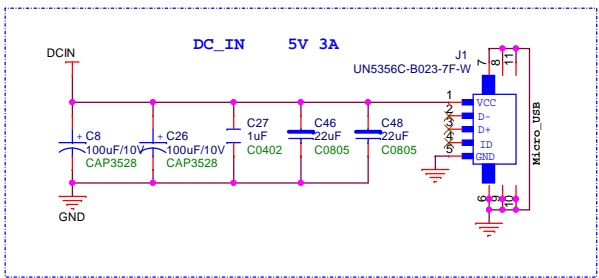
D

C

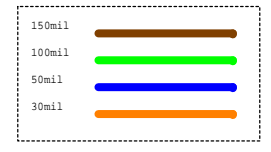
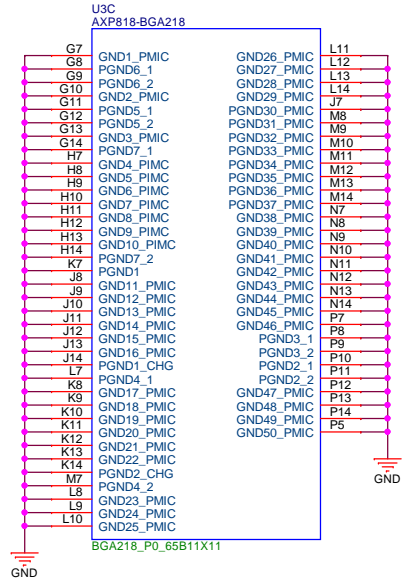
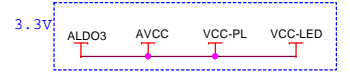
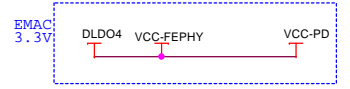
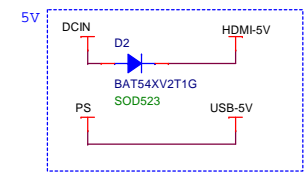
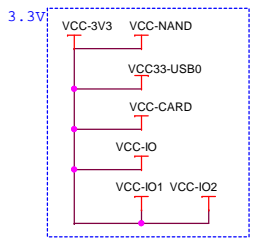
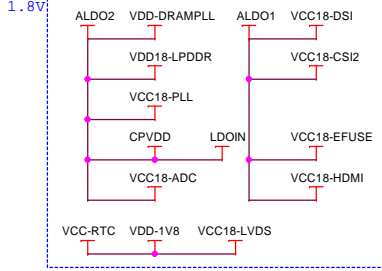
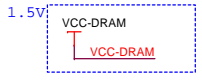
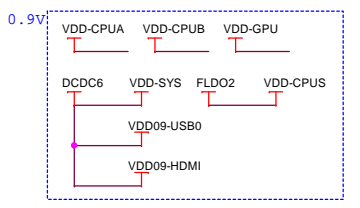
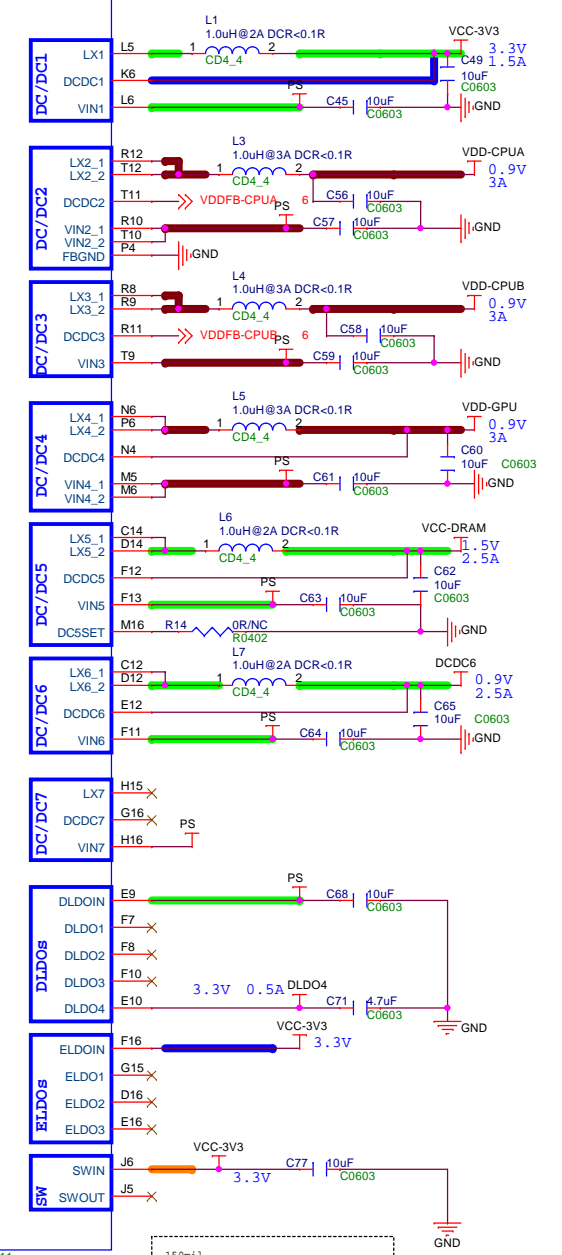
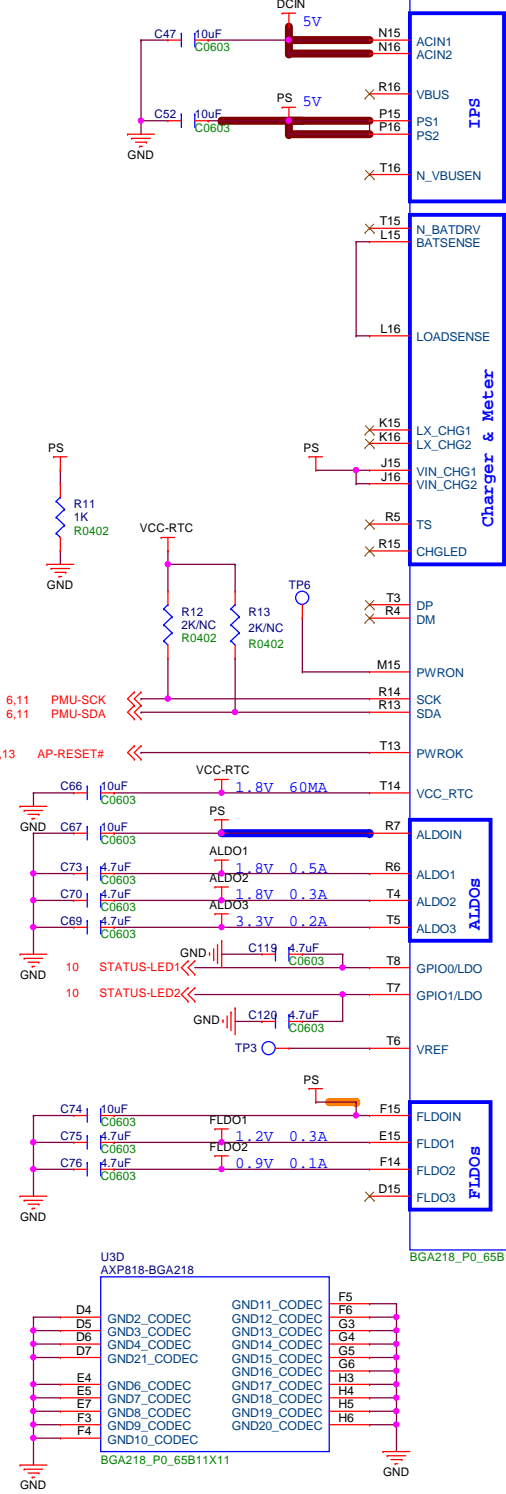
B

A





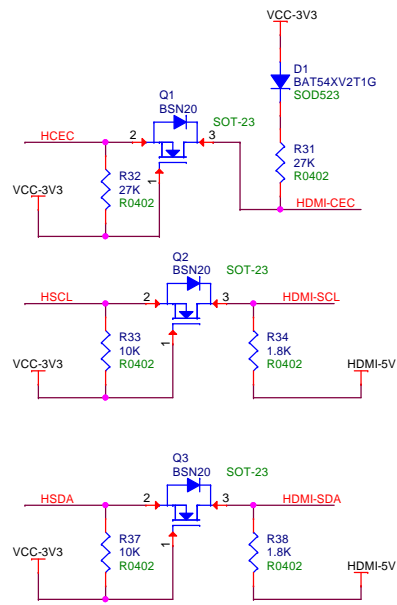
PMIC



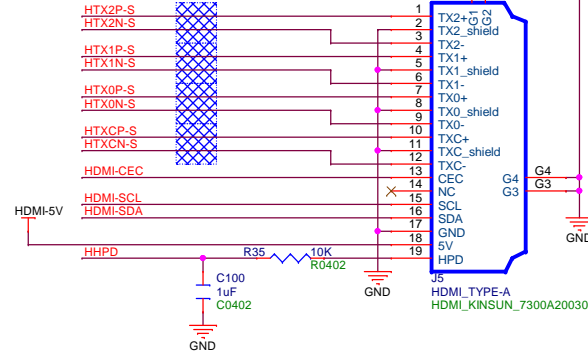
File: pcDuino8 Uno			
Size: A3	Document Number: POWER	Sheet: 7	Rev: V01
Date: Wednesday, August 26, 2015			

HDMI

- 6 HTX0P <-> HTX0P-S
- 6 HTX0N <-> HTX0N-S
- 6 HTX1P <-> HTX1P-S
- 6 HTX1N <-> HTX1N-S
- 6 HTX2P <-> HTX2P-S
- 6 HTX2N <-> HTX2N-S
- 6 HTXCP <-> HTXCP-S
- 6 HTXCN <-> HTXCN-S
- 6 HSCL <-> HTXCN-S
- 6 HSDA <-> HTXCN-S
- 6 HHPD <-> HTXCN-S
- 6 HCEC <-> HTXCN-S

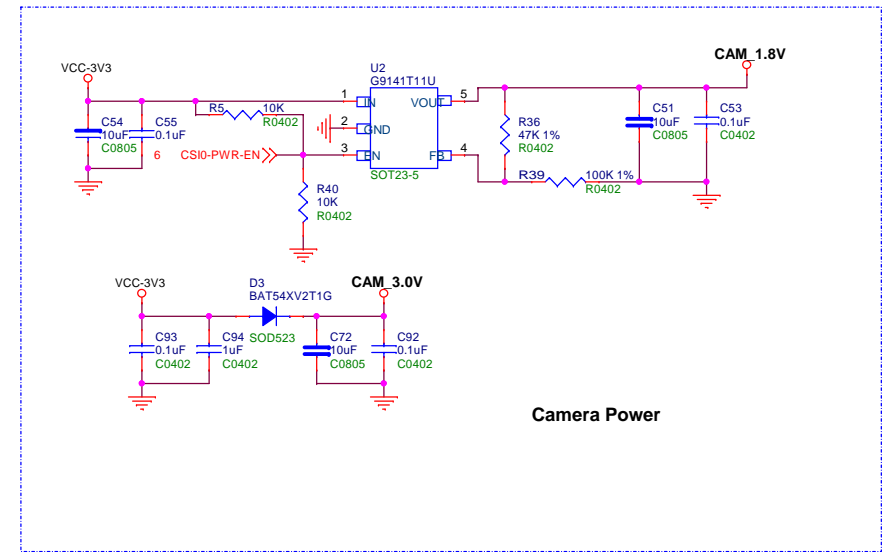
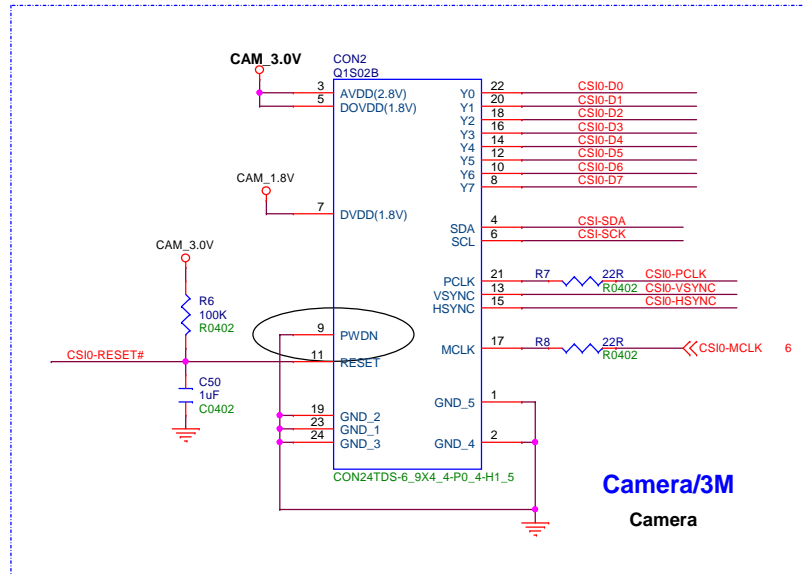
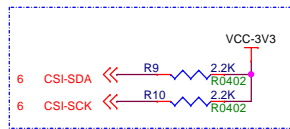


Differential pairs
Z0= 100 ohm



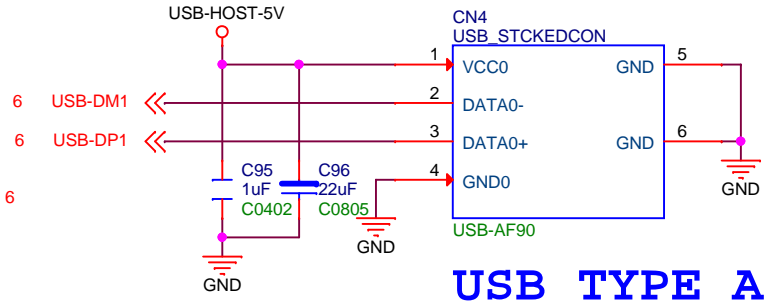
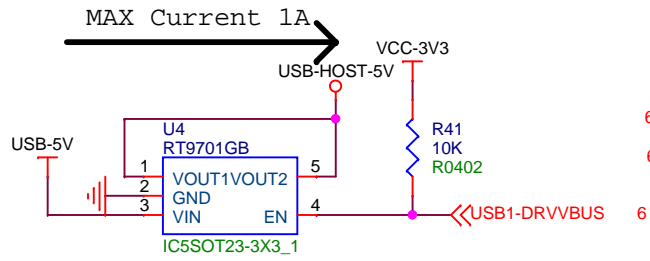
CAMERA

- 6 AP-CSI-D2 <-> CSI0-D0
- 6 AP-CSI-D3 <-> CSI0-D1
- 6 AP-CSI-D4 <-> CSI0-D2
- 6 AP-CSI-D5 <-> CSI0-D3
- 6 AP-CSI-D6 <-> CSI0-D4
- 6 AP-CSI-D7 <-> CSI0-D5
- 6 AP-CSI-D8 <-> CSI0-D6
- 6 AP-CSI-D9 <-> CSI0-D7
- 6 AP-CSI-PCLK <-> CSI0-PCLK
- 6 AP-CSI-HSYNC <-> CSI0-HSYNC
- 6 AP-CSI-VSYNC <-> CSI0-VSYNC
- 6 GM-RST1 <-> CSI0-RESET#



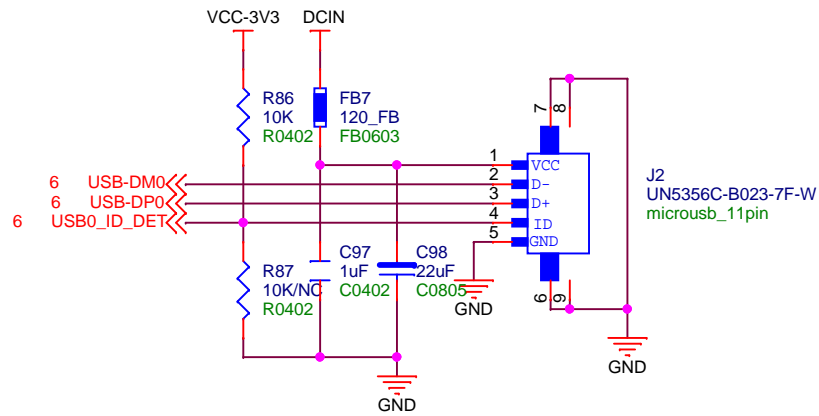
Title		
pcDuino8 Uno		
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USB-HOST0



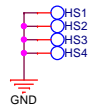
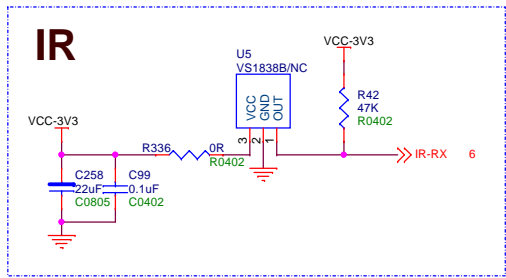
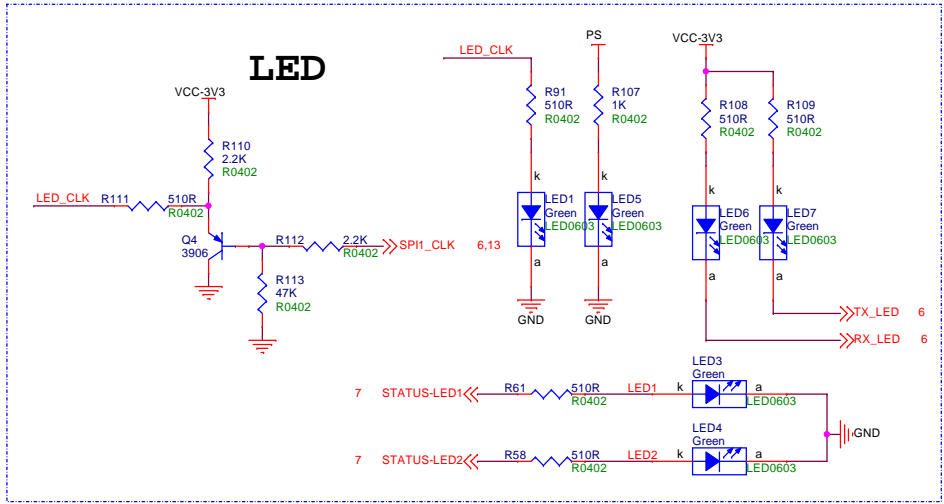
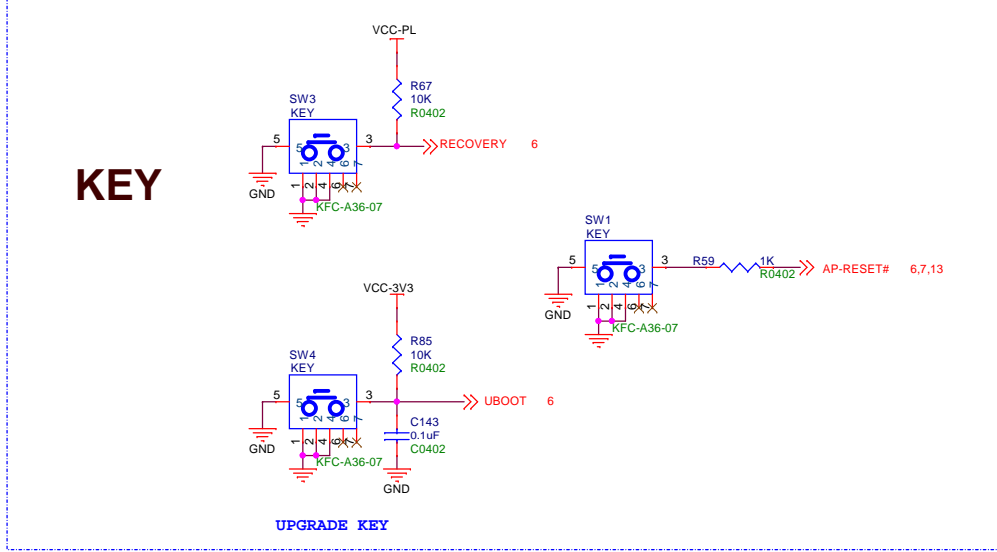
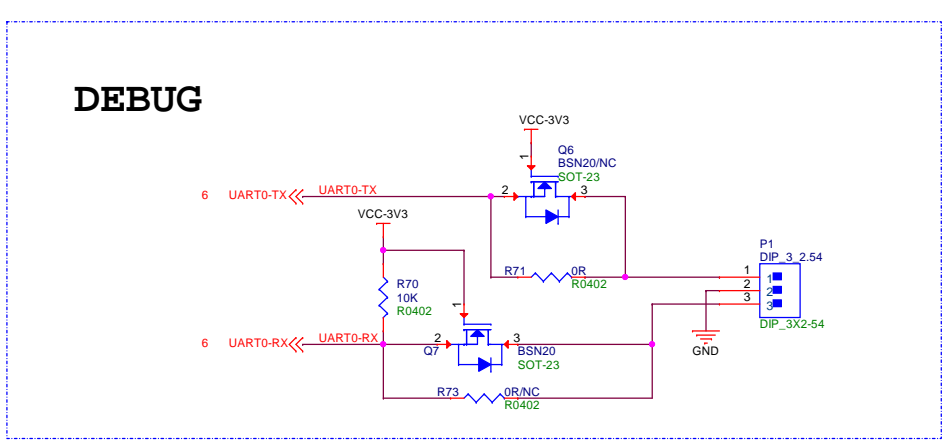
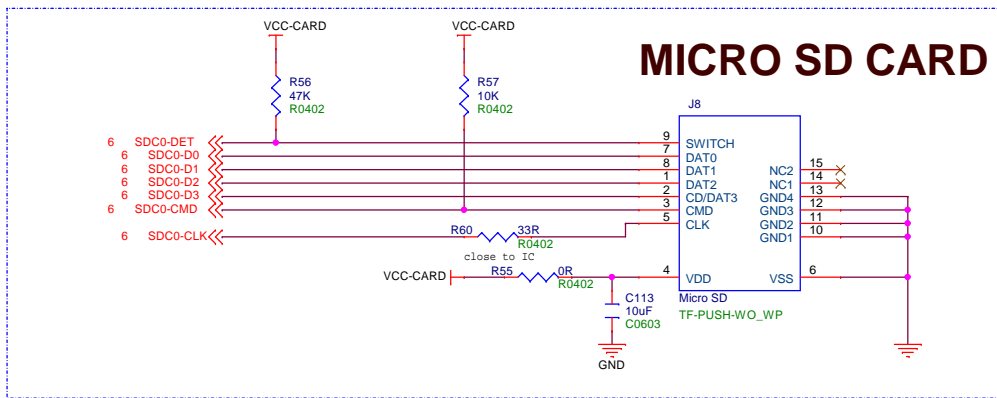
USB TYPE A

USB-OTG



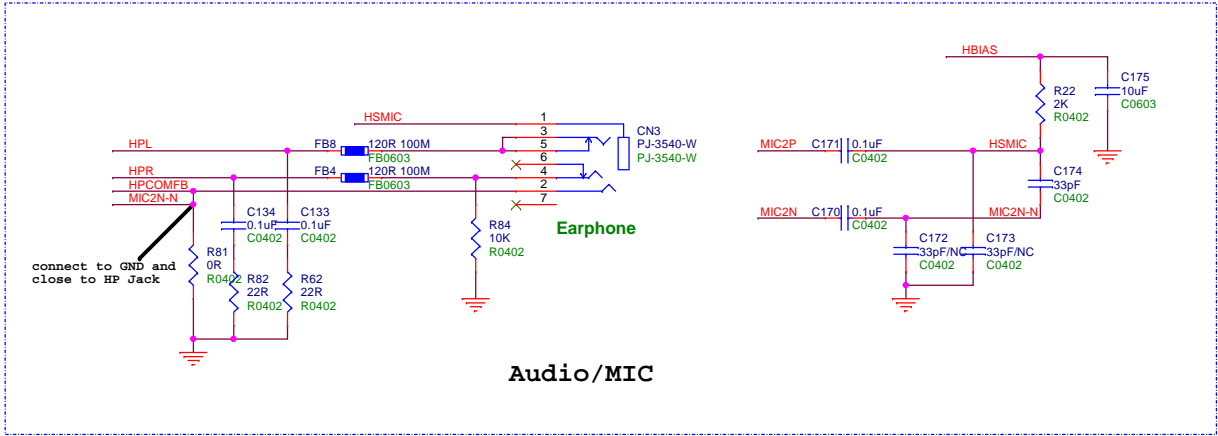
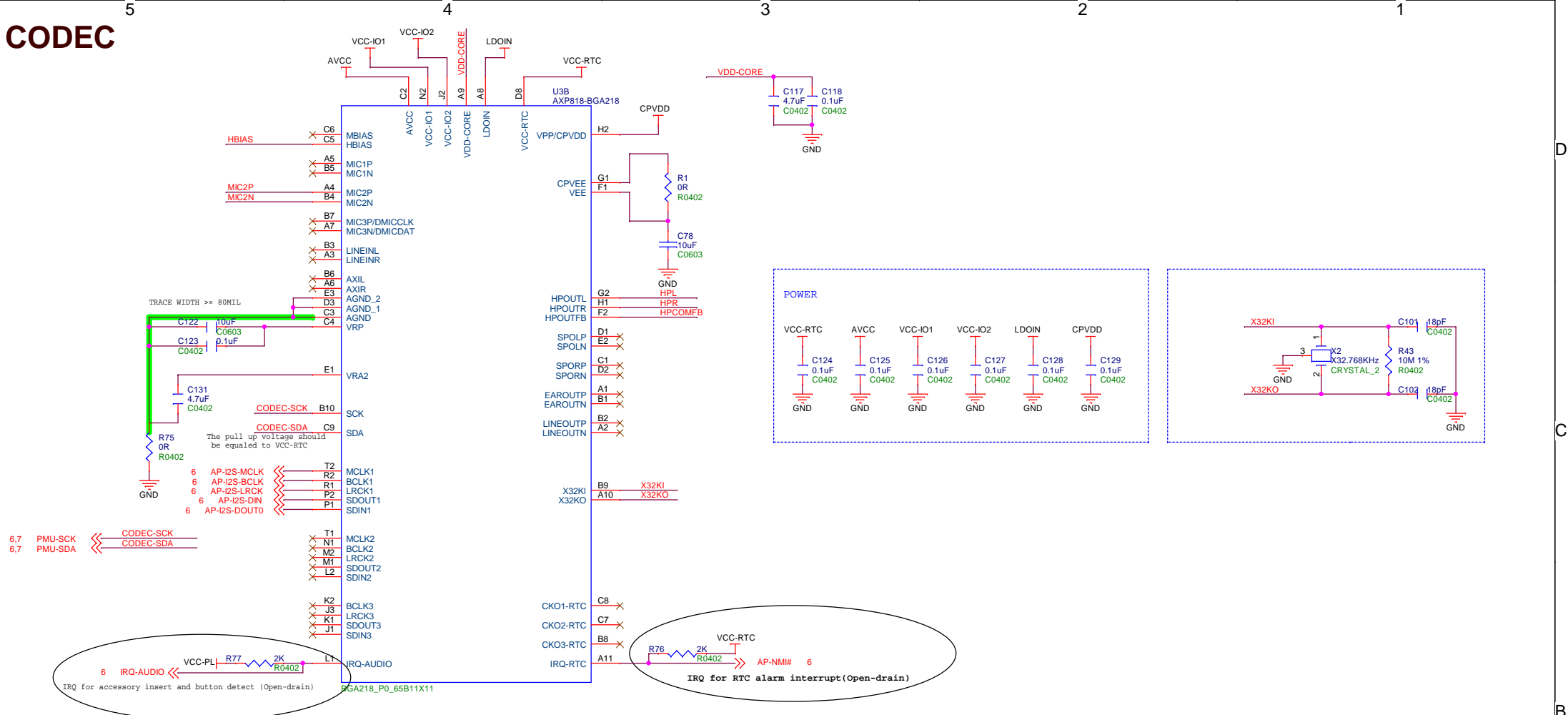
MICRO USB

Title		
pcDuino8 Uno		
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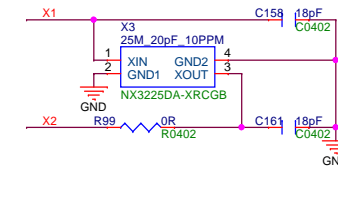
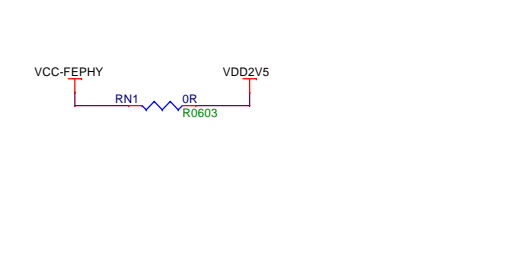
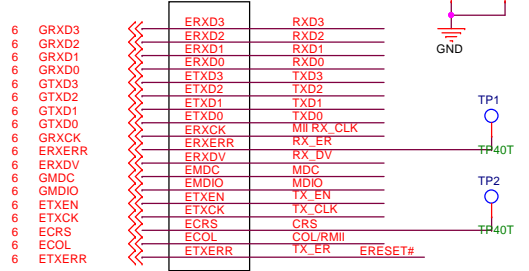
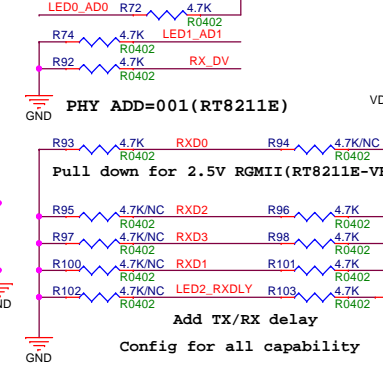
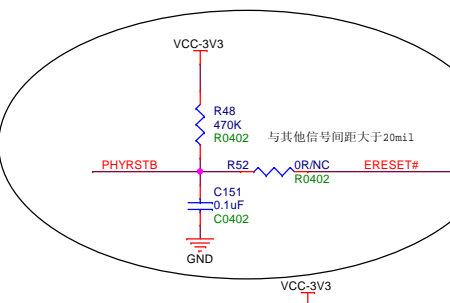
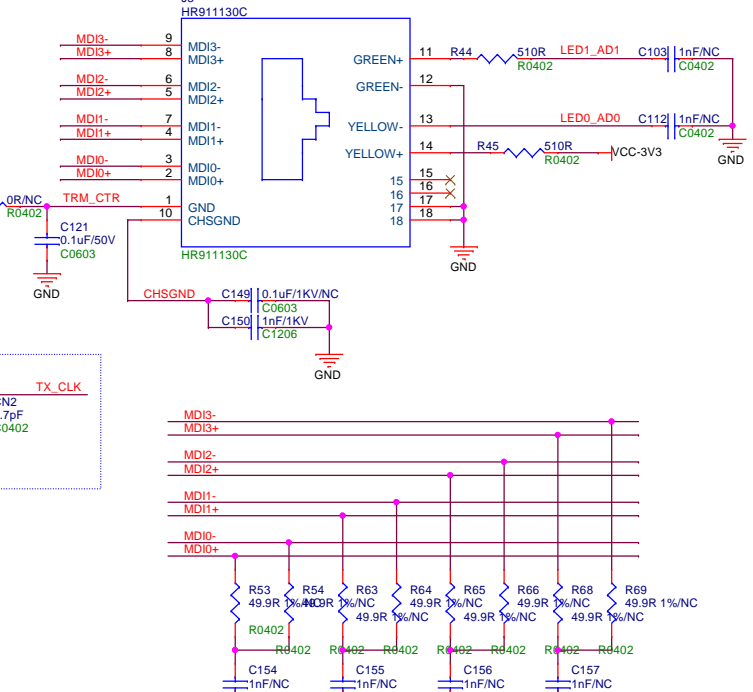
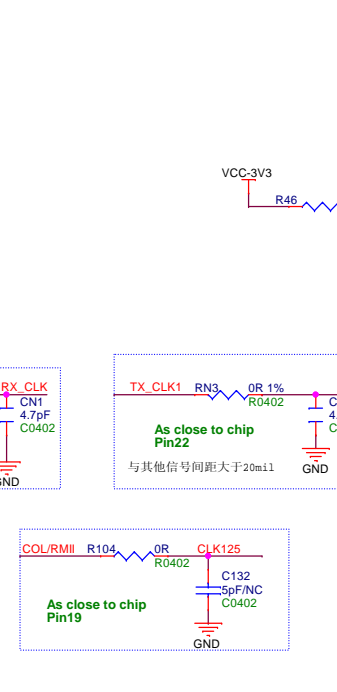
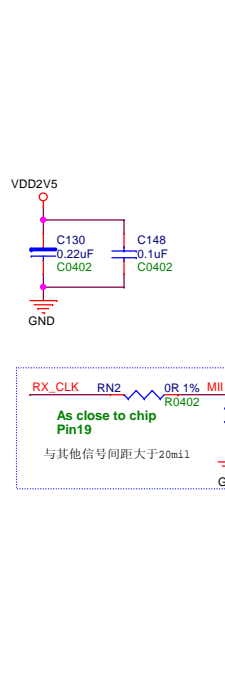
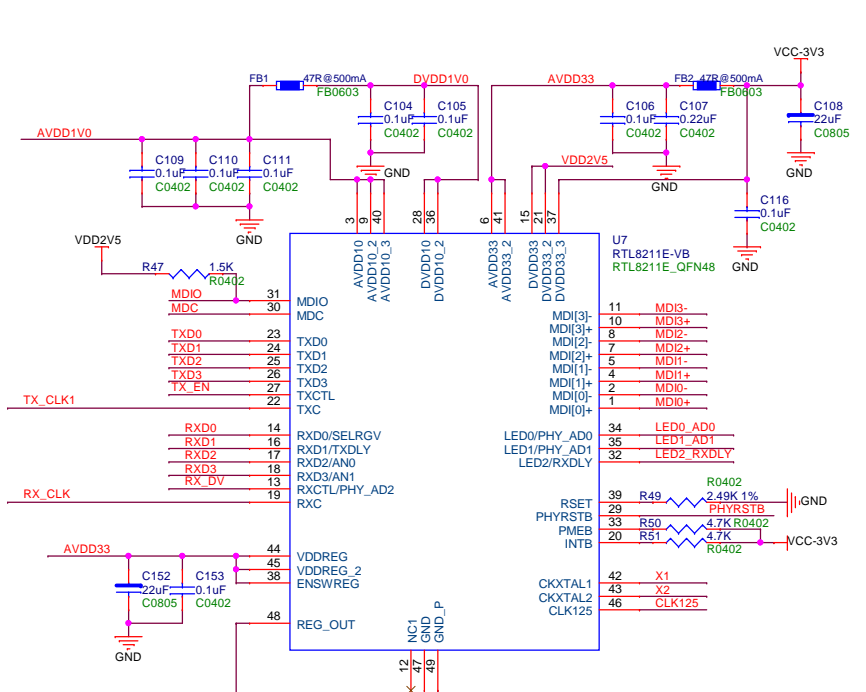


Title			
pcDuino8 Uno			
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A3	CARD-IR-KEY-LED-DEBUG		V01
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CODEC

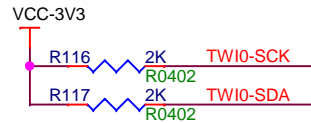
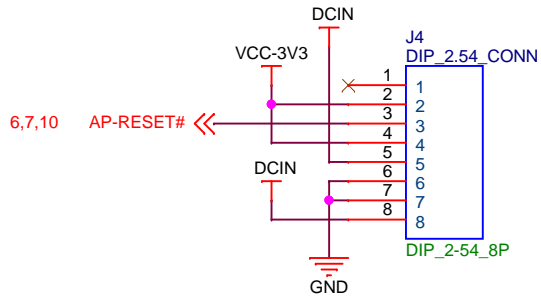


File		
pcDuino8 Uno		
Size	Document Number	Rev
A3	CODEC	V01
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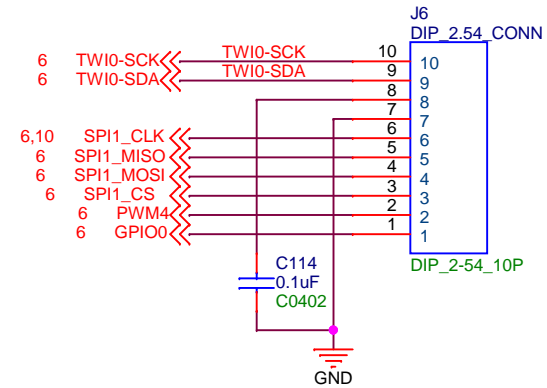


File			
pcDuino8 Uno			
Size	A3	Document Number	MAC-FEPHY
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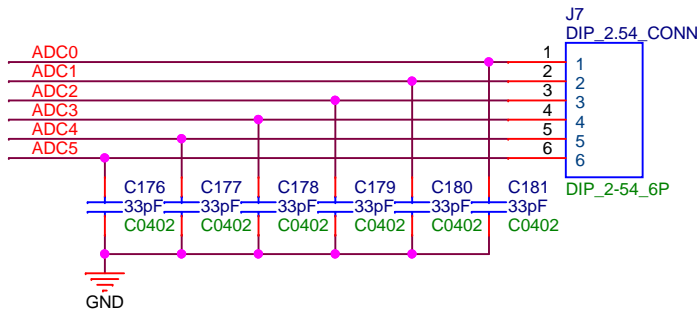
POWER/RESET



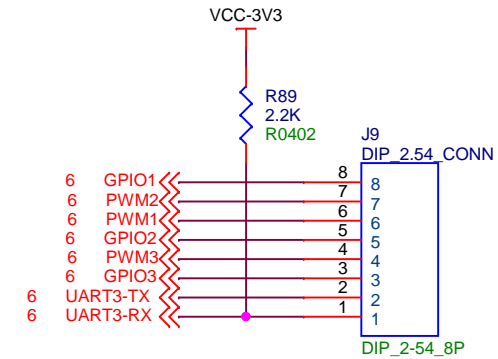
TWI_I2C0/SPI/PWM/GPIO



ADC0-ADC5



GPIO/PWM/UART3



TWI_I2C1 (RESERVED) ----I2C TO ADC MODULE

